

## CLAIMS

1. A reception apparatus comprising:

5 a gain estimation section that estimates a gain for amplifying a received signal in a predetermined reception period to a predetermined reference value for each time slot before said reception period;

10 a gain control section that selects a maximum gain from gains for respective time slots estimated by said gain estimation section and performs gain control over the received signal; and

15 a voltage calibration section that calibrates offset voltage of the received signal before said reception period at said maximum gain selected by said gain control section.

2. The reception apparatus according to claim 1, wherein said gain estimation section estimates said gain for amplifying a received signal of the same frequency  
20 in said reception period made up of consecutive time slots to said reference value.

3. The reception apparatus according to claim 1, further comprising a reception quality measurement  
25 section that finds a measurement value indicating reception quality from a received signal,

wherein said gain estimation section estimates said

gain based on transmit power information comprising information indicating transmit power of each time slot at a communicating party and said measurement value.

5     4.     The reception apparatus according to claim 3,  
         wherein, when a difference between an average gain  
         obtained by averaging said gains and a minimum gain out  
         of said gains is equal to or above a first threshold value  
         in said reception period, said gain estimation section  
10     estimates said gain by excluding said measurement value  
         of the time slot with said minimum gain.

         5.     The reception apparatus according to claim 3,  
         wherein, when a difference between said maximum gain and  
15     the minimum gain out of said gains is equal to or above  
         a second threshold value in said reception period, said  
         gain estimation section estimates said gain by excluding  
         said measured value of the time slot with said minimum  
         gain.

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         6.     The reception apparatus according to claim 3,  
         wherein said gain estimation section subtracts the  
         transmit power of said transmit power information from  
         said measurement value for each time slot and estimates  
25     the transmit power of each time slot and also estimates  
         said gain for amplifying the received signal with the  
         estimated transmit power to said reference value.

7. The reception apparatus according to claim 3, wherein said gain estimation section sets a gain for amplifying a received signal to a predetermined reference value through a plurality of stages for each of said stages during said reception period and sequentially sets gains such that the gain in an earlier one of two consecutive stages is greater than or equal to the gain in a later one of said consecutive stages, and

10       said gain control section performs gain control of a received signal for each of said stages at a gain for each of said stages set by said gain estimation section during said reception period.

15 8. A communication terminal apparatus provided with a reception apparatus, said reception apparatus comprising:

20       a gain estimation section that estimates a gain for amplifying a received signal in a predetermined reception period to a predetermined reference value for each time slot before said reception period;

25       a gain control section that selects a maximum gain from gains for respective time slots estimated by said gain estimation section and performs gain control over the received signal; and

      a voltage calibration section that calibrates offset voltage of the received signal before said

reception period at said maximum gain selected by said gain control section.

9. A reception method comprising steps of:

5       estimating a gain for amplifying a received signal in a predetermined reception period to a predetermined reference value for each time slot before said reception period;

          selecting a maximum gain from gains for respective  
10       estimated time slots and performing gain control over the received signal; and

          calibrating offset voltage of the received signal before said reception period at the selected maximum gain.

15   10. A semiconductor integrated circuit apparatus comprising:

          a gain estimation circuit that estimates a gain for amplifying a received signal in a predetermined reception period to a predetermined reference value for each time  
20       slot before said reception period;

          a gain control circuit that selects a maximum gain from gains for respective time slots estimated by said gain estimation circuit and performs gain control over the received signal; and

25       a voltage calibration circuit that calibrates offset voltage of the received signal before said reception period at said maximum gain selected by said

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gain control circuit.